

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant(s): KIM, Taek Seung

Application No.:

Group:

Filed: December 10, 2001

Examiner:

For: APPARATUS FOR ADJUSTING INPUT CAPACITANCE OF
SEMICONDUCTOR DEVICE AND FABRICATING METHOD

LETTER

COPY

Assistant Commissioner for Patents
Box Patent Application
Washington, D.C. 20231

December 10, 2001
2332-0116P-SP

Sir:

Under the provisions of 35 USC 119 and 37 CFR 1.55(a), the applicant hereby claims the right of priority based on the following application(s):

<u>Country</u>	<u>Application No.</u>	<u>Filed</u>
REPUBLIC OF KOREA	2001-25523	05/10/01

A certified copy of the above-noted application(s) is(are) attached hereto.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. 1.16 or under 37 C.F.R. 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By:

COPY

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KOREAN INTELLECTUAL
PROPERTY OFFICE

Sim, Taek S.
R3h10
703-25200
Dec 10, 2001
청 2332-0116
1 of 1

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별첨 사본은 아래 출원의 원본과 동일함을 증명함.

This is to certify that the following application annexed hereto
is a true copy from the records of the Korean Intellectual
Property Office.

출원번호 : 특허출원 2001년 제 25523 호
Application Number PATENT-2001-0025523

출원년월일 : 2001년 05월 10일
Date of Application MAY 10, 2001

출원인 : 주식회사 하이닉스반도체
Applicant(s) Hynix Semiconductor Inc.

2001 년 07 월 24 일

특 허 청

COMMISSIONER

